



IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Janet Patterson

SERIAL NO.: 10/621,844

FILED: July 16, 2003

TITLE: Packaging Of Semiconductor Devices For Increased Reliability

EXAMINER: Ngo, H.

ART UNIT: 2831

ATTY DKT NO: 73537(1328)

---

MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VIRGINIA 22313-1450

**REQUEST FOR A TWO MONTH EXTENSION OF TIME**

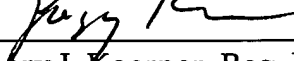
SIR:

Applicants hereby request two month extension of time in responding to the Office Action mailed September 15, 2004. Enclosed herewith is a check for the \$430.00 filing fee for the extension of time. Please charge our deposit Account No. 50-1443 for any additional fees that may be incurred. A duplicate copy of this letter is enclosed for this purpose.

Respectfully submitted,

Janet Patterson

Date: 12/10/04

By:   
Gregory J. Koerner, Reg. No. 38,519  
REDWOOD PATENT LAW  
1291 East Hillsdale Boulevard, Suite 205  
Foster City, California 94404  
TEL: (650) 358-4000